

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(parallel near3 (switch\$3 or transistor\$2 or FET))with (multiphas\$2 near6 (N adj2 phas\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:21
L2	0	(parallel near3 (switch\$3 or transistor\$2 or FET))with (multi\$phas\$2 near6 (N adj2 phas\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:22
L3	2	(parallel near3 (switch\$3 or transistor\$2 or FET))with (multi\$phas\$2 near6 (data adj2 signal\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:25
L4	2	(parallel near3 (switch\$3 or transistor\$2 or FET))with (multi\$phas\$2 near4 (data adj2 signal\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:24
L5	0	(parallel near3 (switch\$3 or transistor\$2 or FET))with (multi\$phas\$2 near4 (data adj2 signal\$2))	EPO; JPO; DERWENT	OR	ON	2005/03/28 16:24
L6	0	(parallel near3 (switch\$3 or transistor\$2 or FET))same (multi\$phas\$2 near4 (data adj2 signal\$2))	EPO; JPO; DERWENT	OR	ON	2005/03/28 16:24
L7	0	(parallel near3 (switch\$3 or transistor\$2 or FET))and (multi\$phas\$2 near4 (data adj2 signal\$2))	EPO; JPO; DERWENT	OR	ON	2005/03/28 16:25
L8	0	((plural\$4 or parallel) near3 (switch\$3 or transistor\$2 or FET))and (multi\$phas\$2 near4 (data adj2 signal\$2))	EPO; JPO; DERWENT	OR	ON	2005/03/28 16:44
L9	2	((plural\$4 or parallel or multiple) near3 (switch\$3 or transistor\$2 or FET))with (multi\$phas\$2 near6 (data adj2 signal\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:28

L10	4	("5767699" "5949253").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:42
L11	0	JP2900059199	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:32
L12	0	JP-2900059199	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:32
L13	1	JP2000059199	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:34
L14	59	heiken.inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:36
L15	9	14 and samsung	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:35
L16	30	heiken.inv.	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:36
L17	113323	(control\$4 or vary\$3 or adjust\$4)near6 (output near2 voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:54
L18	0	I17 same (((((plural\$4 or multiple)near4 parallel)) near3 (switch\$3 or transistor\$2 or FET))and (multi\$phas\$2 near4 (data adj2 signal\$2)))	EPO; JPO; DERWENT	OR	ON	2005/03/28 16:46

L19	0	I17 same ((((((plural\$4 or multiple)near4 parallel))) near3 (switch\$3 or transistor\$2 or FET))and (multi\$phas\$2 near4 (data adj2 signal\$2))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:46
L20	0	I17 same ((((((plural\$4 or multiple)near4 parallel))) near3 (switch\$3 or transistor\$2 or FET))same (multi\$phas\$2 near4 (data adj2 signal\$2))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:47
L21	2	I17 and ((((((plural\$4 or multiple)near4 parallel))) near3 (switch\$3 or transistor\$2 or FET))same (multi\$phas\$2 near4 (data adj2 signal\$2))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:48
L22	2	I17 and ((((((plural\$4 or multiple)near4 parallel))) near3 (switch\$3 or transistor\$2 or FET))and (multi\$phas\$2 near4 (data adj2 signal\$2))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:48
L23	2	((((((plural\$4 or multiple)near4 parallel))) near3 (switch\$3 or transistor\$2 or FET))with(multi\$phas\$2 near4 (data adj2 signal\$2))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:49
L24	2	((((((plural\$4 or multiple)near4 parallel))) with (switch\$3 or transistor\$2 or FET))same(multi\$phas\$2 near4 (data adj2 signal\$2))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:50
L25	105065	"327"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:50
L26	1136	I25 and ((((((plural\$4 or multiple)near4 parallel))) with (switch\$3 or transistor\$2 or FET)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:51

L27	2	I26 and (multi\$phas\$2 near4 (data adj2 signal\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:51
L28	24372	"713"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:50
L29	47	I28 and (((plural\$4 or multiple)near4 parallel)) with (switch\$3 or transistor\$2 or FET))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:51
L30	0	I29 and (multi\$phas\$2 near4 (data adj2 signal\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:51
L31	2	I29 and ((multi\$phas\$2 or delay\$3) near4 (data adj2 signal\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:52
L32	21	I26 and ((multi\$phas\$2 or delay\$3) near4 (data adj2 signal\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:55
L33	10	I29 and ((control\$4 or vary\$3 or adjust\$4)near6 (output near2 voltage))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:54
L34	12	I32 and ((control\$4 or vary\$3 or adjust\$4)near6 (output near2 voltage))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:54

L35	0	I33 and ((multi\$phas\$2 or delay\$3) near4 (data adj2 signal\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:55
L36	8	I33 and ((multi\$phas\$2 or delay\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:55
L37	12	I34 and ((multi\$phas\$2 or delay\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 16:59